

Amendments to the specification and claims are presented herein by presenting replacement paragraphs for the specification, along with a complete set of pending claims, as amended, in clean form. Also, an Appendix entitled "Version With Markings to Show Changes Made," showing the current amendments to the specification and claims is attached hereto.

Please amend the above-identified application as follows:

**IN THE SPECIFICATION:**

Delete the paragraph beginning at page 2, line 1, and ending at page 2, line 7, and replace with the following:

*a<sup>1</sup>* As shown in Fig. 16, each pixel has a photoelectric conversion circuit composed of a PN-junction photodiode PD that functions as a photosensitive means and a MOS transistor T100 that has its drain and gate connected to the anode of the photodiode PD. A direct-current voltage VPD is applied to the cathode of the photodiode PD and a direct-current voltage VPS is applied to the MOS transistor T100 so that the MOS transistor T100 is so biased as to operate in a subthreshold region.

Delete the paragraph beginning at page 7, line 18, and ending at page 8, line 3, and replace with the following:

*a<sup>2</sup>* To the signal lines 11-1 to 11-m are connected, respectively, switches S1-1 to S1-m and switches S2-1 to S2-m. Through the switches S1-1 to S1-m, image signals are fed from the signal lines 11-1 to 11-m to capacitors C1-1 to C1-m, respectively. On the other hand, through the switches S2-1 to S2-m, noise signals are fed from the signal lines 11-1 to 11-m to capacitors C2-1 to C2-m, respectively. The image signals fed to the capacitors C1-1 to C1-m and sampled and held therein are then fed through the buffers 13a-1 to 13a-m, respectively, to the output circuit 14. On the other hand, the noise signals fed to the capacitors C2-1 to C2-m

Concld  
A2

and sampled and held therein are then fed through the buffers 13b-1 to 13b-m, respectively, to the output circuit 14.

Delete the paragraph beginning at page 27, line 24, and ending at page 28, line 14, and replace with the following:

A3

The R, G, and B signals fed to the color temperature detection circuit 5 are fed respectively to the integrator circuits 51, 52, and 53 so as to be integrated. Here, integration of these signals is achieved by adding together the signal levels of the color signals of each color for a predetermined time period, for example for the time period in which image signals corresponding to one frame is output, i.e. by adding together the signal levels of the color signals obtained from an identical number of pixels for each color. The R and G signals thus integrated by the integrator circuits 51 and 52 are fed respectively to the input terminals "a" and "b" of the comparator circuit 54, and the G and B signals thus integrated by the integrator circuits 52 and 53 are fed respectively to the input terminals "b" and "a" of the comparator circuit 55. These comparator circuits 54 and 55, using as a reference signal the G signal fed to their respective input terminals "b", detect the signal levels of the R and B signals. Detecting the signal levels of the R and B signals relative to that of the G signal in this way constitutes detecting the color temperature of the subject being sensed.

**IN THE CLAIMS:**

Please replace the previous version of the claims with the following clean version, wherein claim 1 incorporates new amendments thereto.